

#7/21a
TUC
1-230
PATENT APPLICATION
Q51885

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Akitaka KIMURA

Appln. No.: 09/161,981

Filed: September 29, 1998



Group Art Unit: 2881

Examiner: J. Davie

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For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND
METHOD FOR DEPOSITING SEMICONDUCTOR LAYER

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated October 10, 2000 in which the Examiner has required a restriction, Applicants respond by electing Group I, *i.e.*, claims 1-4, 9-17 and 24-26, drawn to a method, classified in class 438, subclass 29, without traverse.

Prompt and favorable action on the elected claims are now respectfully requested.

Respectfully submitted,

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Date: December 11, 2000